

----- Simulation Segment -----

SIMULATION

TRACE_ON VFB_I NO_NMI DPB_I IOP_I ACON_I TIMER D7 D6 D5 D4 D3 CS_INT
IPL0 IPL1 IPL2

;Initialize Inputs

SETF /D3 /D4 /D5 /D6 /D7 CS_INT NO_NMI /VFB_I /DPB_I /IOP_I /ACON_I
/TIMER

;Simulation Sequence

SETF VFB_I ;VFB interrupt
;not captured since NO_NMI signal is asserted
SETF /VFB_I /NO_NMI ;deassert NO_NMI
SETF VFB_I ;VFB interrupt again
;captured and output
SETF /VFB_I DPB_I ;DPB interrupt
;captured but not output since VFB event pending
SETF /DPB_I D7
SETF /CS_INT ;clears VFB event
;now DPB code is output
SETF CS_INT IOP_I ;IOP interrupt
;captured but not output since DDP event pending
SETF /IOP_I /D7 D6
SETF /CS_INT ;clears pending DPB event
;now IOP code is output
SETF CS_INT ACON_I ;ACON interrupt
;captured but not output since IOP event pending
SETF /ACON_I /D6 D5
SETF /CS_INT ;clears pending IOP event
;now ACON code is output
SETF CS_INT TIMER ;Timer interrupt
;captured but not output since ACON event pending
SETF /D5 D4
SETF /CS_INT ;clears pending ACON event
;now Timer code is output
SETF CS_INT /D4 D3
SETF /CS_INT ;clears Timer event
SETF CS_INT

;simulation done

TRACE_OFF
